

FIG. 1

FIG. 2

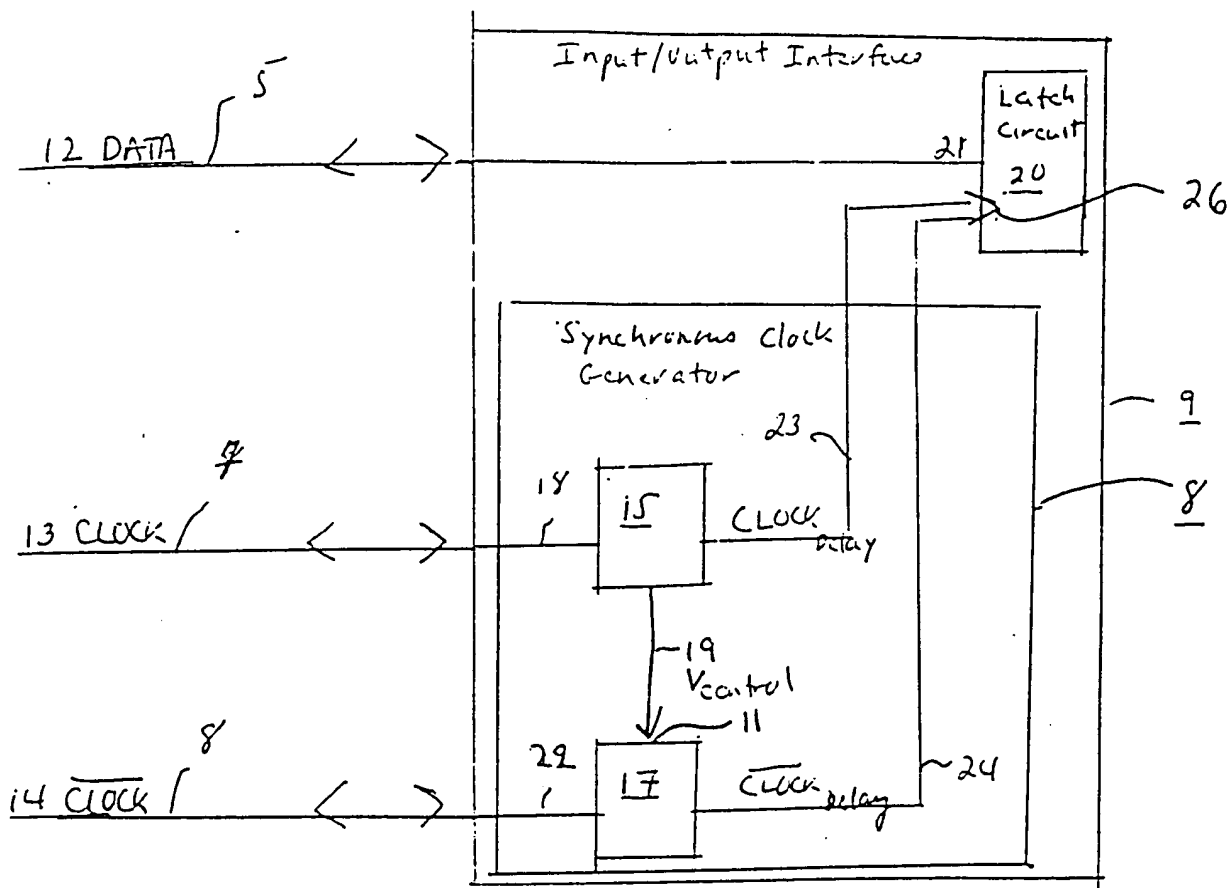


FIG. 2

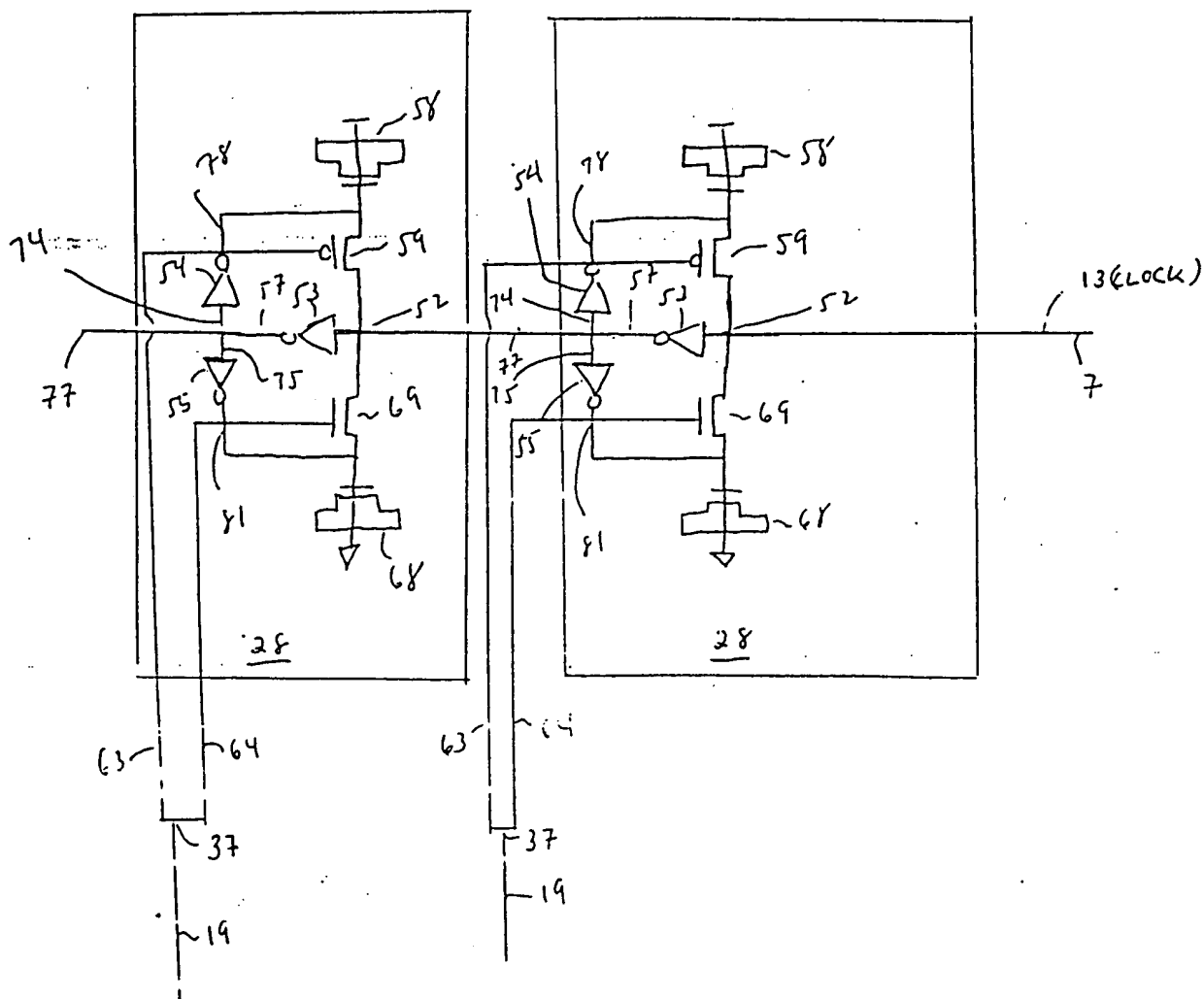
The image contains two hand-drawn block diagrams. The top diagram is titled "Delay Lock Loop Circuit" and the bottom diagram is titled "Delay Circuit".

Delay Lock Loop Circuit: This circuit is enclosed in a rectangular box. It features a horizontal chain of six blocks, each labeled "28". The input to the first block is labeled "13". The output of the sixth block is labeled "35". Below this chain, there is a block labeled "PD" (with "22" below it) and a block labeled "Filter" (with "38" below it). The output of the "Filter" block is labeled "19" and is connected to a block labeled "V_{control}". The "V_{control}" block is connected to the bottom of each of the six "28" blocks. The output of the "PD" block is labeled "31".

Delay Circuit: This circuit is also enclosed in a rectangular box. It features a horizontal chain of six blocks, each labeled "48". The input to the first block is labeled "14". The output of the sixth block is labeled "45". Below this chain, there is a block labeled "V_{control}" (with "11" below it). The "V_{control}" block is connected to the bottom of each of the six "48" blocks. The output of the fourth block is labeled "49".

FIG. 3

FIG. 4



FOOTNOTES

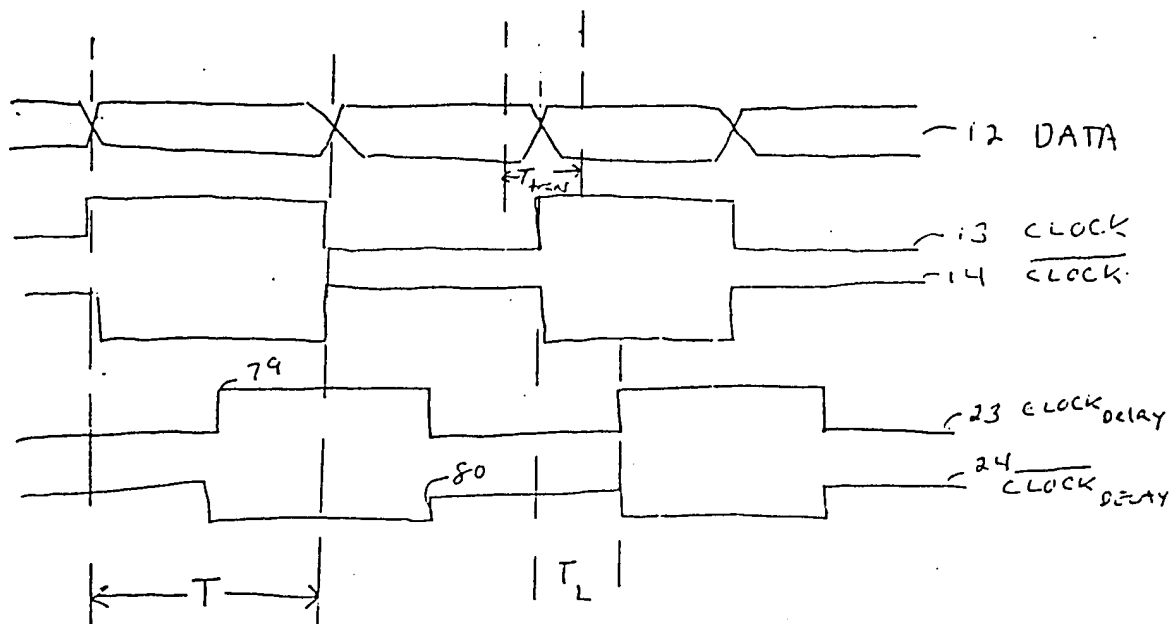


FIG. 5